



INTELLECTUAL PROPERTY
402-391-4448

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IFW

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June 29, 2004

Commissioner for Patents
Box: 1450
Alexandria, VA 22313-1450

RE: APPLICATION OF WELCH TITLED: "SEMICONDUCTOR DEVICES IN
COMPENSATED SEMICODUCTOR";
SERIAL NO.: 10/603,334;
FILE DATE: 06/26/03;
ART UNIT: 2811;
EXAMINER: LOKE.

RESPONSE TO REQUIREMENT OF RESTRICTION

Dear Sir;

I am in Receipt of a Requirement of Restriction dated
06/24/04. The Examiner requires an election of Species and
identifies groupings relatd to:

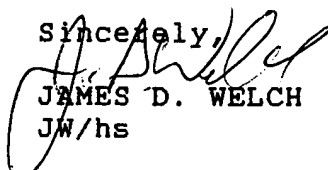
Figs. 6a and 6b; or
Figs. 7a and 7b.

In view of Issued Patent 6,624,693 which Claims what is
shown in Figs. 7a and 7b, Applicant elects Figs. 6a and 6b, with
strong Traverse on the basis that, as established in prosecution
of the 693 Patent, which Issued without Terminal Disclaimer, the
Examiner has no prior art on which to base a rejection of any
Semiconductor Device whatsoever which is fabricated in
Compensatated Semiconductor.

Applicant notes that a corresponding PCT Application
(PCT/US03/29675) identified only Patent 6,268,636 to Welch and
Patent 4,300,152 to Lepselter, both of which are known to the
Examiner. The PCT Preliminary Examination, signed by Examiner
Eddie Lee, included a copy of Patent No. 6,624,493 to Welch, but
found that the criteria for Novelty, Inventive Step and
Industrial Application were met.

Applicant identifies Claims 1 - 4 and 22 - 24 as reading, at
least in part, on Figs. 7a and 7b, but emphasizes that Claims 1
and 2 are Generic in view of the recitation of "Compensated
Semiconductor", therefore all Claims should be Allowed.

Sincerely,


JAMES D. WELCH
JW/hs